



Substitute for Form 1449A/PTO (Modified)

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Complete if Known

Application Number	10/057,159
Filing Date	January 24, 2002
First Named Inventor:	Norman C. Chou, et al.
Group Art Unit	2131
Examiner Name	Not Yet Assigned
Attorney Docket Number	5288.P017

Sheet

1

Of

1

OTHER ART - NO PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T
DBL		"InfiniBand Switch Chip Runs at 10 Gbps On Eight Ports", Nicholas Cravotta, 11/8/2001, EDN, 1 page	
DBL		"Assemble Fast Switch Fabrics With 32-Port InfiniBand Node p. 60", Electronic Design, 10/15/2001, 4 pages	
DBL		"RedSwitch, Inc. Announces Industry's Highest Performance and Highest Integration InfiniBand Switch Chip", RedSwitch Press Release, 10/16/2001, 2 pages.	
DBL		"RedSwitch Gearing Up To Launch New Chip", Steve Tanner, Silicon Valley Business Ink, 10/26/2001, 3 pages.	
DBL		"Mellanox Integrates Serdes Into Infiniband Switch", Jerry Ascierto, EE Times, 10/23/2001, 3 pages.	
DBL		"Switch Chip Expands InfiniBand Integration", EEM File 3130, Tony Chance, 2 pages.	
DBL		"RedSwitch Announces 16 Gbyte/s Throughout Switch Product for RapidIO Architecture", RedSwitch Press Release, Milpitas, Calif., May 15, 2001, Tony Chance, 5/15/2001, 2 pages.	
DBL		"RedSwitch and Agilent Technologies Unveil 160-GB/s Throughout Switch Product for InfiniBand Architecture", RedSwitch Press Release, Intel Developer Forum Conference, San Jose, Calif., Feb. 27, 2001, Mark Alden-Agilent, Tony Chance-RedSwitch, 2 pages.	

RECEIVED

OCT 09 2002

Technology Center 2100

Examiner Signature	David Garcia Cervantes	Date Considered	6/14/2005
--------------------	------------------------	-----------------	-----------

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Unique citation designation number. ²Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.